



DIM500XSM65-TS000

Single Switch IGBT Module

Replaces DS6173-4 August 2017 (LN34635)

FEATURES

- 10µs Short Circuit Withstand
- High Thermal Cycling Capability
- Soft Punch Through Silicon
- Isolated AlSiC Base With AlN Substrates
- Lead Free Construction

APPLICATIONS

- High Reliability Inverters
- Motor Controllers
- Traction Drives
- Choppers

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 1200V to 6500V and currents up to 2400A.

The DIM500XSM65-TS000 is a single switch 6500V, n-channel enhancement mode, insulated gate bipolar transistor (IGBT) module. The IGBT has a wide reverse bias safe operating area (RBSOA) plus 10µs short circuit withstand. This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

ORDERING INFORMATION

Order As:

DIM500XSM65-TS000

Note: When ordering, please use the complete part number

KEY PARAMETERS

V _{CES}		6500V
V _{CE(sat)} *	(typ)	2.8V
I _C	(max)	500A
I _{C(PK)}	(max)	1000A

* Measured at the auxiliary terminals

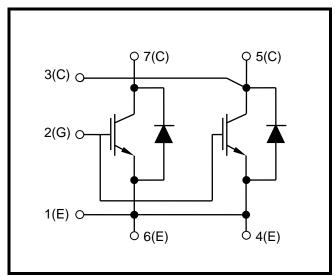


Fig. 1 Circuit configuration



Fig. 2 Package



ABSOLUTE MAXIMUM RATINGS

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
		V _{GE} = 0V, T _j = 125°C	6500	V
V_{CES}	Collector-emitter voltage	$V_{GE} = 0V, T_j = 25^{\circ}C$	6500	V
		$V_{GE} = 0V, T_j = -40^{\circ}C$	6000	V
V_{GES}	Gate-emitter voltage		±20	V
I _C	Continuous collector current	T _{case} = 100°C	500	Α
I _{C(PK)}	Peak collector current	1ms, T _{case} = 115°C	1000	Α
P _{max}	Max. transistor power dissipation	$T_{case} = 25^{\circ}C, T_{j} = 125^{\circ}C$	7.4	kW
l ² t	Diode I ² t value	$V_R = 0$, $t_p = 10$ ms, $T_j = 125$ °C	90	kA ² s
V _{isol}	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	10.2	kV
Q_{PD}	Partial discharge – per module	IEC1287, V ₁ = 6900V, V ₂ = 5100V, 50Hz RMS	10	рC

THERMAL AND MECHANICAL RATINGS

Internal insulation material:

Baseplate material:

Creepage distance:

Clearance:

CTI (Comparative Tracking Index):

AIN

AISiC

56mm

26mm

> 600

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
R _{th(j-c)}	Thermal resistance – transistor	Continuous dissipation - junction to case	-	-	13.5	°C/kW
R _{th(j-c)}	Thermal resistance – diode	Continuous dissipation - junction to case	-	-	27	°C/kW
R _{th(c-h)}	Thermal resistance – case to heatsink (per module)	Mounting torque 5Nm (with mounting grease)	-	1	8	°C/kW
T _j	Junction temperature	Transistor	-	-	125	°C
		Diode	-	-	125	°C
T _{stg}	Storage temperature range	-	-40	ı	125	°C
	Screw torque	Mounting – M6	-	-	5	Nm
		Electrical connections – M4	-	-	2	Nm
		Electrical connections – M8	-	-	10	Nm



ELECTRICAL CHARACTERISTICS

 T_{case} = 25°C unless stated otherwise.

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
I _{CES}	Collector cut-off current	$V_{GE} = 0V$, $V_{CE} = V_{CES}$			1	mA
		$V_{GE} = 0V$, $V_{CE} = V_{CES}$, $T_{case} = 125$ °C			60	mA
I _{GES}	Gate leakage current	$V_{GE} = \pm 20V, V_{CE} = 0V$			1	μA
V _{GE(TH)}	Gate threshold voltage	$I_C = 120$ mA, $V_{GE} = V_{CE}$	5.5	6.5	7.5	V
\	Collector-emitter saturation voltage	V _{GE} = 15V, I _C = 500A		2.8		V
V _{CE(sat)} †		$V_{GE} = 15V$, $I_C = 500A$, $T_j = 125$ °C		3.7		V
I _F	Diode forward current	DC			500	Α
I _{FM}	Diode maximum forward current	t _p = 1ms			1000	Α
\	Diode forward voltage	I _F = 500A		3.3		V
V_F^{\dagger}		I _F = 500A, T _j = 125°C		3.7		V
C _{ies}	Input capacitance	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$		8		nF
Q_g	Gate charge	±15V		7		μC
C _{res}	Reverse transfer capacitance	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$		1.6		nF
L_M	Module inductance			20		nΗ
R _{INT}	Internal transistor resistance			180		μΩ
SC _{Data}	Short circuit current, I _{SC}	$T_{j} = 125^{\circ}\text{C}, \ V_{CC} = 4400\text{V},$ $t_{p} \le 10\mu\text{s}, \ V_{GE} \le 15\text{V}$ $V_{CE \ (max)} = V_{CES} - L^{*} x \ dI/dt$ IEC 60747-9		2500		А

Note:

[†] Measured at the auxiliary terminals

L is the circuit inductance + L_M



ELECTRICAL CHARACTERISTICS

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t _{d(off)}	Turn-off delay time	$I_{C} = 500A$ $V_{GE} = \pm 15V$		3.7		μs
t _f	Fall time			450		ns
E _{OFF}	Turn-off energy loss	V _{CE} = 3600V		2900		mJ
t _{d(on)}	Turn-on delay time	$R_{G(ON)} = 2.2\Omega$ $R_{G(OFF)} = 12\Omega$ $C_{ge} = 220nF$ $L_{S} \sim 200nH$		620		ns
t _r	Rise time			340		ns
E _{ON}	Turn-on energy loss			3900		mJ
Q_{rr}	Diode reverse recovery charge	I _F = 500A		1000		μC
I _{rr}	Diode reverse recovery current	V _{CE} = 3600V		1000		Α
E _{rec}	Diode reverse recovery energy	$dI_F/dt = 2800A/\mu s$		1800		mJ

T_{case} = 125°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t _{d(off)}	Turn-off delay time	I _C = 500A		3.9		μs
t _f	Fall time	$V_{GE} = \pm 15V$		450		ns
E _{OFF}	Turn-off energy loss	V _{CE} = 3600V		3000		mJ
t _{d(on)}	Turn-on delay time	$R_{G(ON)} = 2.2\Omega$ $R_{G(OFF)} = 12\Omega$		550		ns
t _r	Rise time	C _{ge} = 220nF		300		ns
E _{ON}	Turn-on energy loss	L _S ~ 200nH		4700		mJ
Q_{rr}	Diode reverse recovery charge	I _F = 500A		1700		μC
I _{rr}	Diode reverse recovery current	V _{CE} = 3600V		1600		Α
E _{rec}	Diode reverse recovery energy	$dI_F/dt = 2800A/\mu s$		2900		mJ



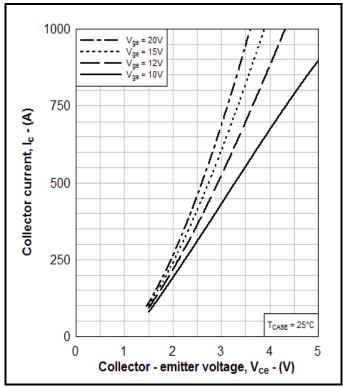


Fig. 3 Typical output characteristics

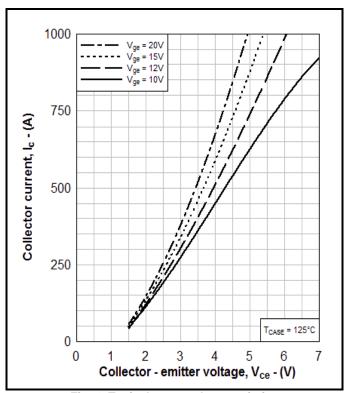


Fig. 4 Typical output characteristics

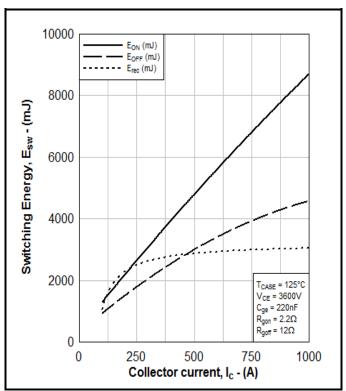


Fig. 5 Typical switching energy vs collector current

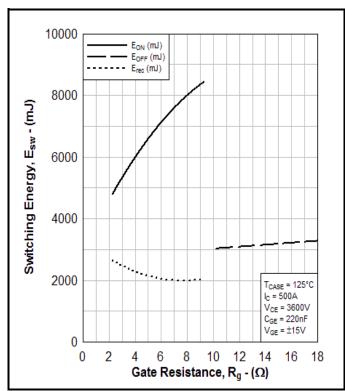


Fig. 6 Typical switching energy vs gate resistance



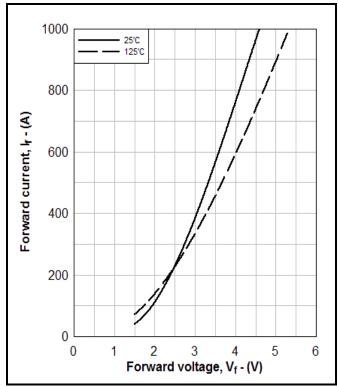


Fig. 7 Diode typical forward characteristics

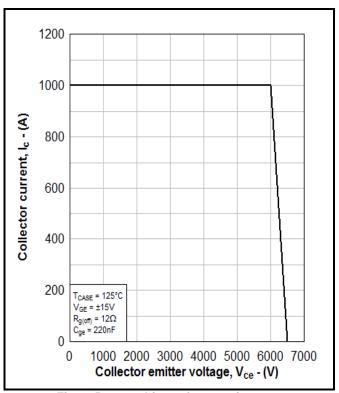


Fig. 8 Reverse bias safe operating area

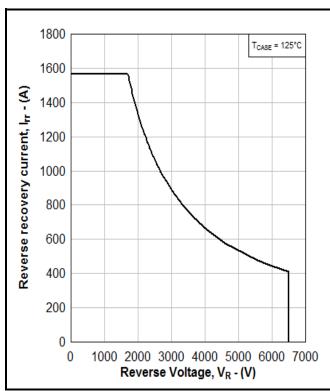


Fig. 9 Diode reverse bias safe operating area

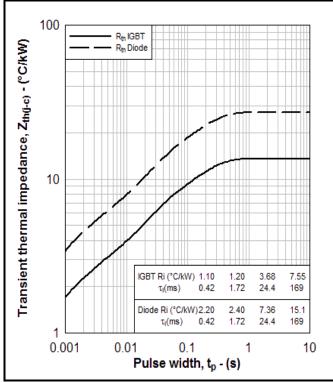


Fig. 10 Transient thermal impedance



PACKAGE DETAILS

For further package information, please visit our website or contact Customer Services. All dimensions in mm, unless stated otherwise.

DO NOT SCALE.

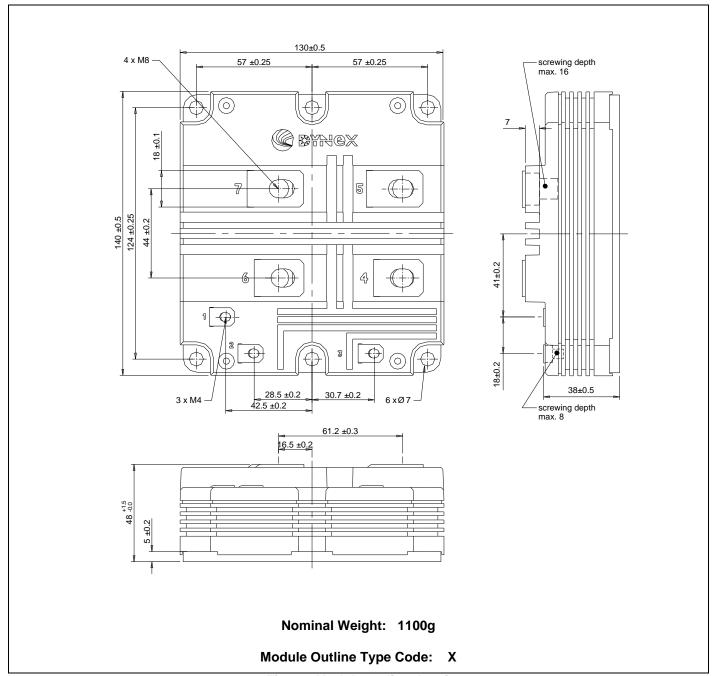


Fig. 11 Module outline drawing



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